

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MURATA, et al.

Filed: December 3, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
HAVING SWITCHING MISFET AND CAPACITOR
ELEMENT AND METHOD OF PRODUCING THE SAME,
INCLUDING WIRING THEREFOR AND METHOD OF
PRODUCING SUCH WIRING

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

December 3, 2001

Sir:

Please amend the above-identified application, prior to examination thereof,
as follows:

IN THE SPECIFICATION

Please delete the paragraph beginning at page 1, line 1, of the specification,
and substitute therefor the following new paragraph:

--This application is a Continuation application of application Serial No.
09/915,590, filed July 27, 2001, which is a Continuation application of application
Serial No. 09/317,999, filed May 25, 1999, now U.S. Patent No. 6,281,071, which

is a Divisional application of application Serial No. 09/013,605, filed January 26, 1998, now U.S. Patent No. 5,930,624, which is a Continuation application of application Serial No. 08/620,867, filed March 25, 1996, now U.S. Patent No. 5,753,550, which is a Continuation application of application Serial No. 08/254,562, filed June 6, 1994, now U.S. Patent No. 5,504,029, which is a Divisional application of application Serial No. 07/894,351, filed June 4, 1992, now abandoned, which is a Divisional application of application Serial No. 07/246,514, filed September 1988, now U.S. Patent No. 5,153,685.--

REMARKS

Applicants have amended the specification of the above-identified Continuation application to satisfy the requirements of 35 USC 120, identifying prior applications, of the above-identified application, which are being relied upon under 35 USC 120 in the above-identified application.

In connection with the claims of the present application, note claims 1, 3, 5, 6, 7, 21 and 22, respectively, of U.S. Patent No. 6,281,071. Note also that the present claims, as compared with claims of U.S. Patent No. 6,281,071, recite that first electrode for capacitor element of the recited semiconductor integrated circuit device is formed to extend over the gate electrode. In addition, the independent claims of the present application recite that the patterned dielectric film is formed so as not to extend over the other of the source and drain regions of the MISFETs, (that is, the source region or drain region to which the first electrode is not connected).

For the sole purpose of facilitating proceedings in connection with the present application, a Terminal Disclaimer is submitted herewith, with respect to U.S. Patent No. 6,281,071.

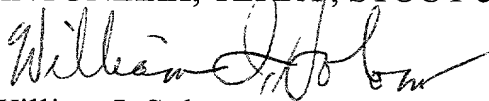
Attached hereto is a marked-up version of the changes made to the specification by the current Amendment. This marked-up version is on the

attached pages, the first page of which is captioned "VERSION WITH
MARKINGS TO SHOW CHANGES MADE".

To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.26244CC7) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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WIS/slk

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION

Please delete the paragraph beginning at page 1, line 1, of the specification, and substitute therefor the following new paragraph:

[This application is a Continuation application of application Serial No. 09/317,999, filed May 25, 1999, which is a Divisional application of application Serial No. 09/013,605, filed January 26, 1998, now U.S. Patent No. 5,930,624, which is a Continuation application of application Serial No. 08/620,867, filed March 25, 1996, now U.S. Patent No. 5,753,550, which is a Continuation application of application Serial No. 08/254,562, filed June 6, 1994, now U.S. Patent No. 5,504,029, which is a Divisional application of application Serial No. 07/894,351, filed June 4, 1992, now abandoned, which is a Divisional application of application Serial No. 07/246,514, filed September 19, 1988, now U.S. Patent No. 5,153,685.]

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